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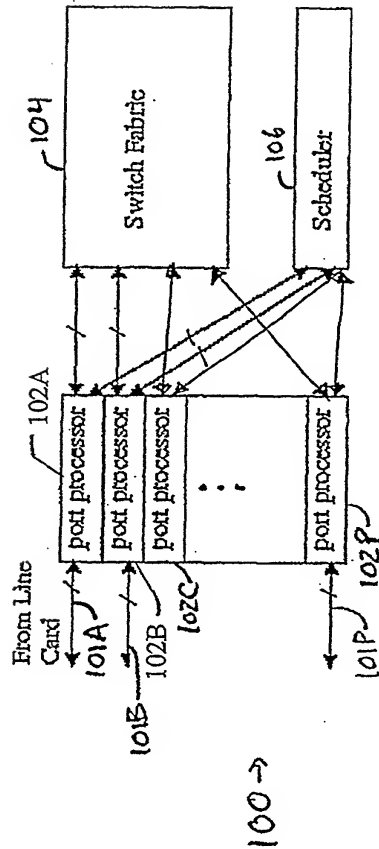


Figure 1

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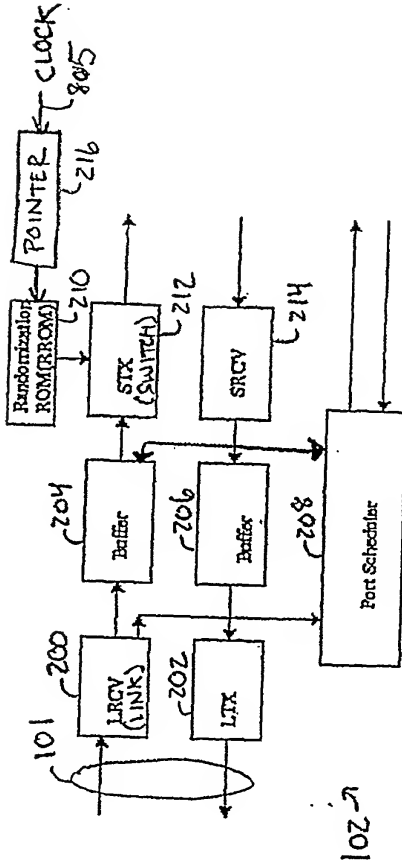


FIGURE 2

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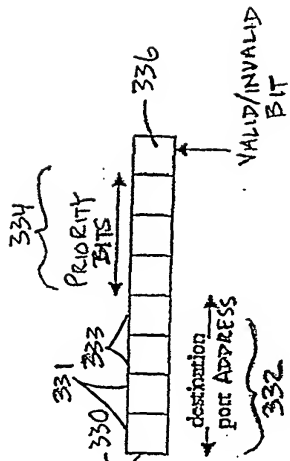


FIGURE 3A

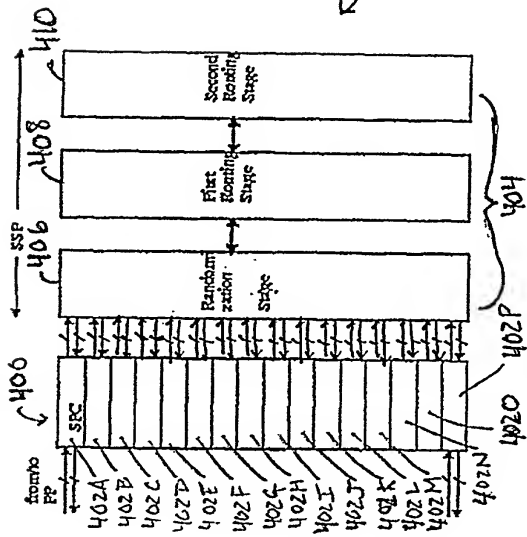
Queue splitting:

8	0	4	0	300
			5	302
			6	304
			2	306
			3	308
15	11	3	7	310
				312
				314
				316
				318
				320

FIGURE 3B

FIGURE 4

2106



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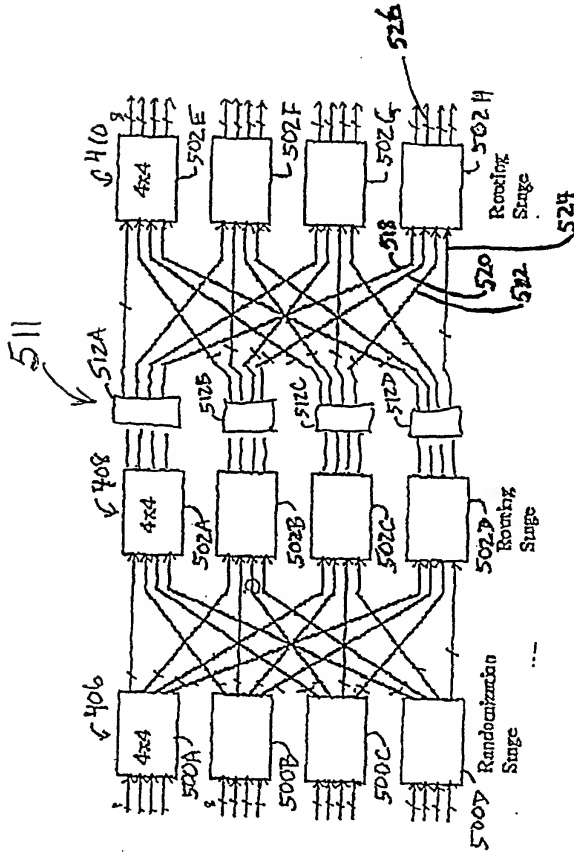


FIGURE 5
 404 →

202110-05615001

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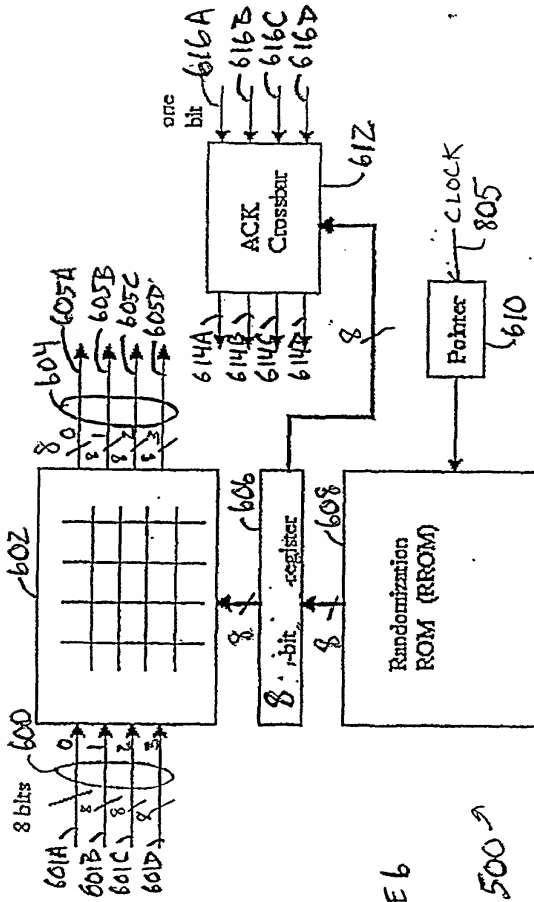


FIGURE 6

500

↑
sps 4007

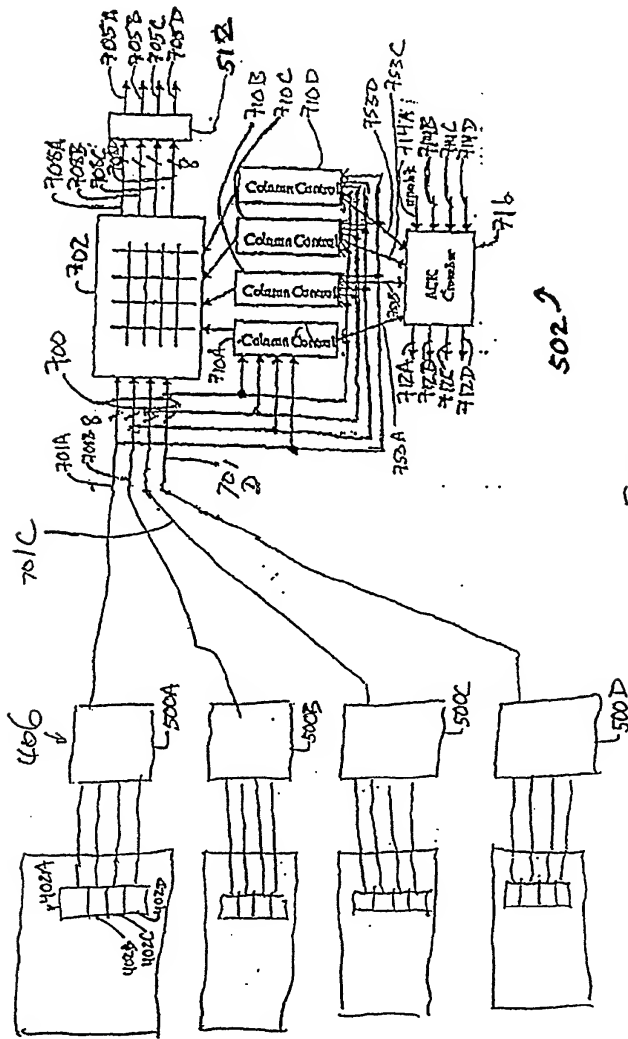


Figure 7A

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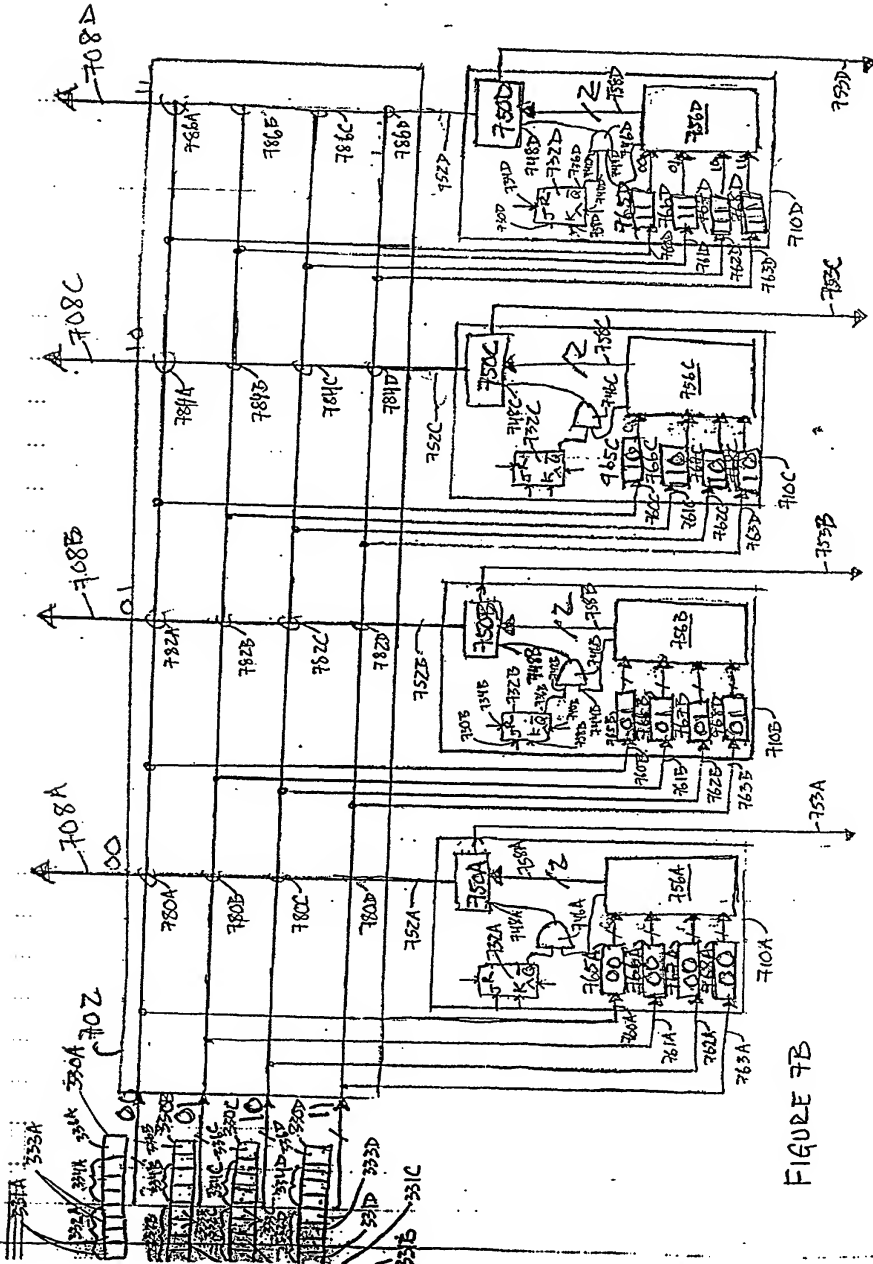
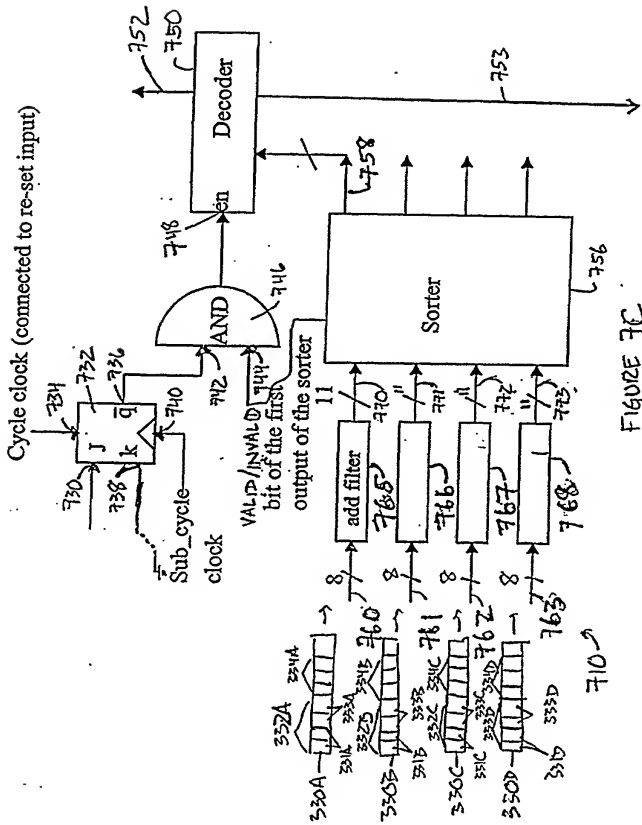


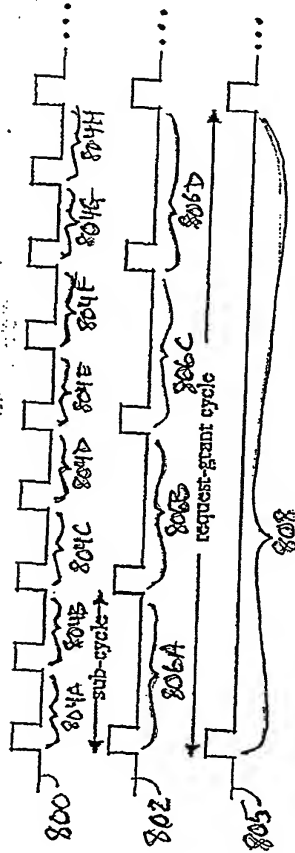
FIGURE 7B

209110-06615001

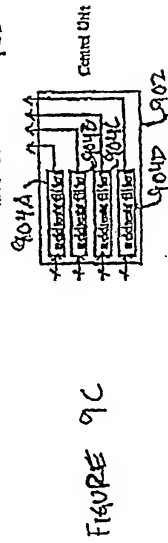
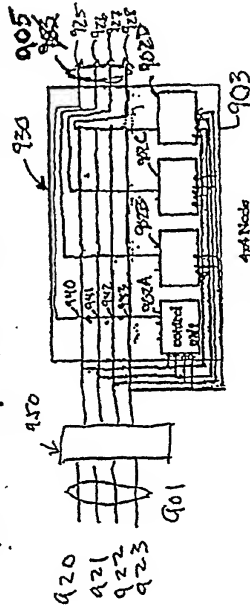
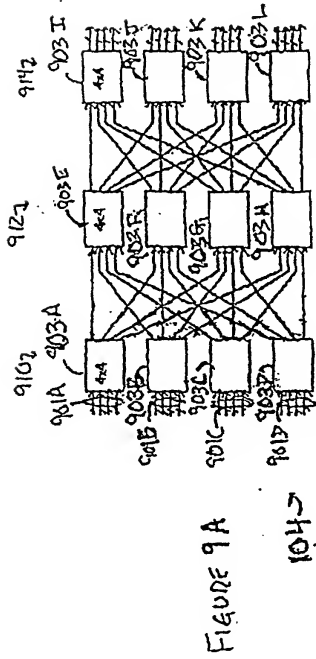
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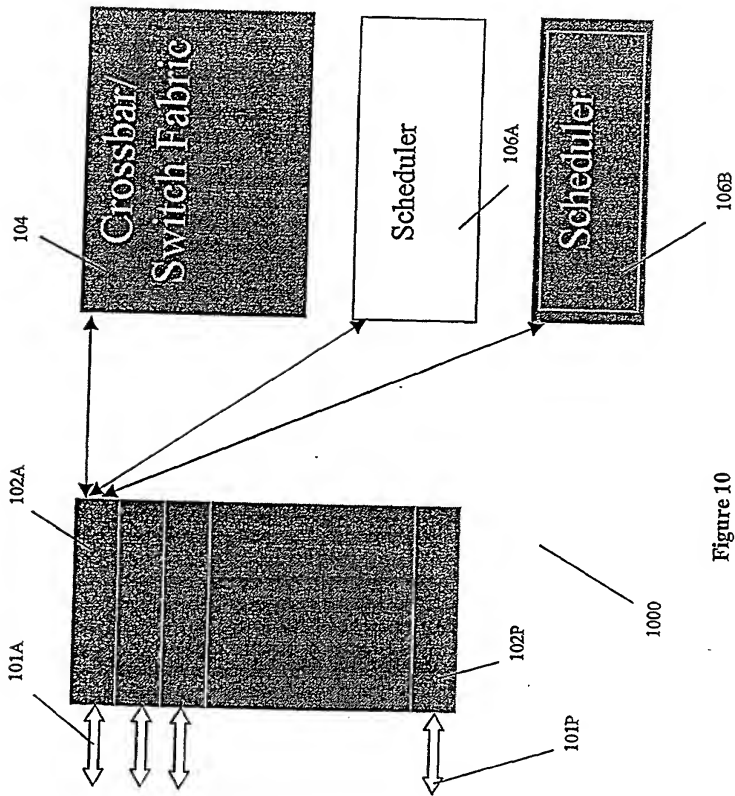


Figure 10

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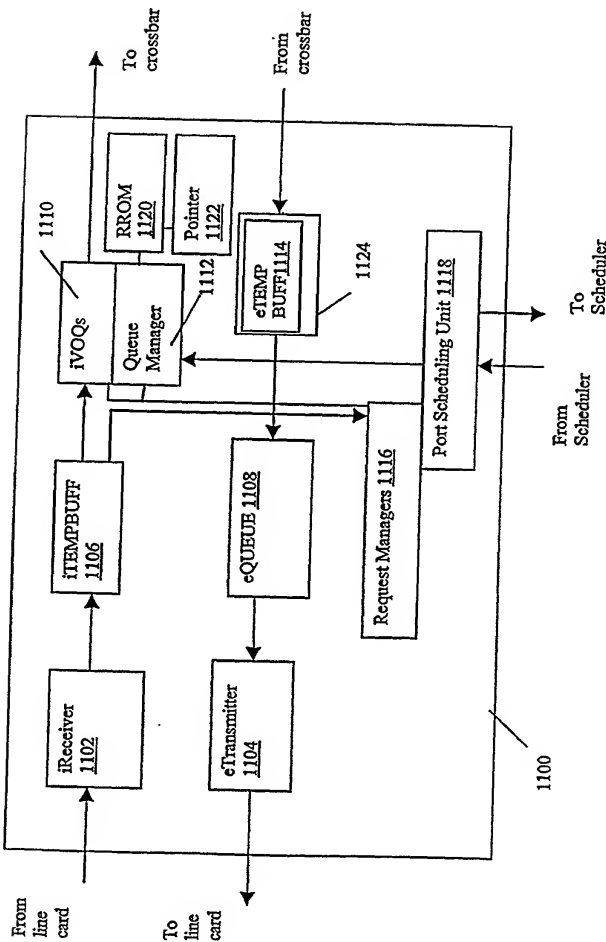
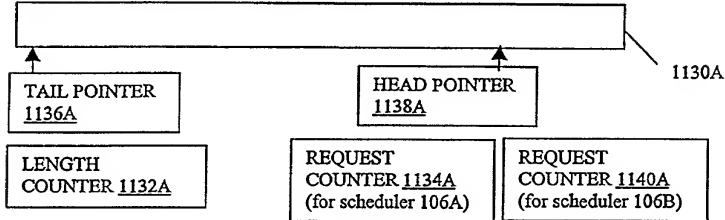


Figure 11A

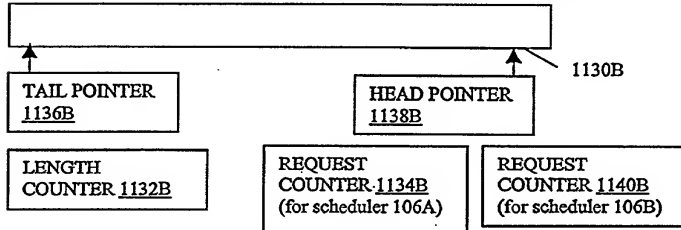
20251107 06:15:01

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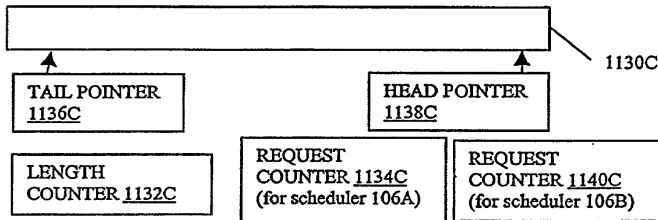
VOQ FOR DESTINATION PORT ADDRESS 0000000000, PRIORITY BITS = 000



VOQ FOR DESTINATION PORT ADDRESS 0000000000, PRIORITY BITS = 001



VOQ FOR DESTINATION PORT ADDRESS 0000000001, PRIORITY BITS = 000



VOQ FOR DESTINATION PORT ADDRESS 0000000001, PRIORITY BITS = 001

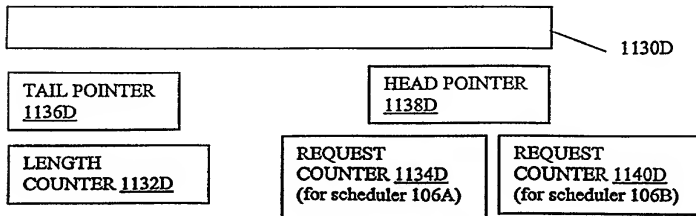


FIGURE 11B

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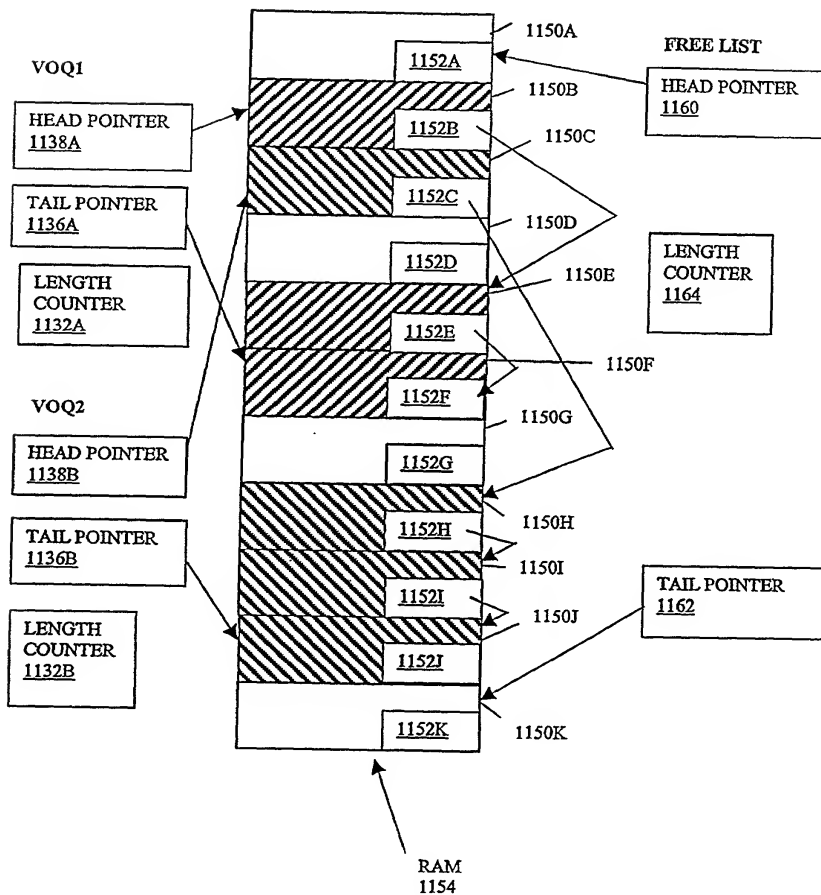


FIGURE 11C

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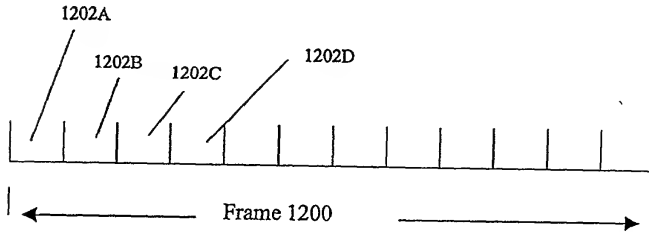


Figure 12

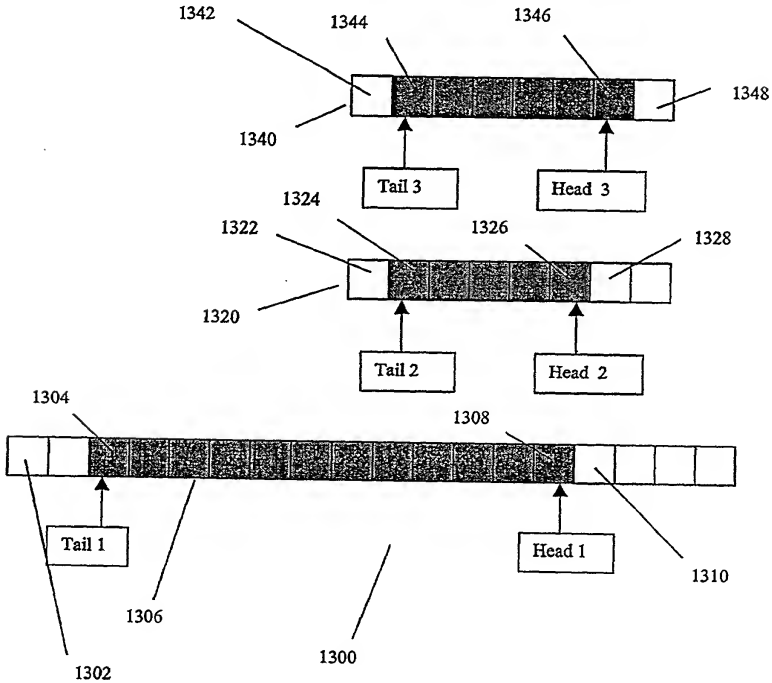


Figure 13

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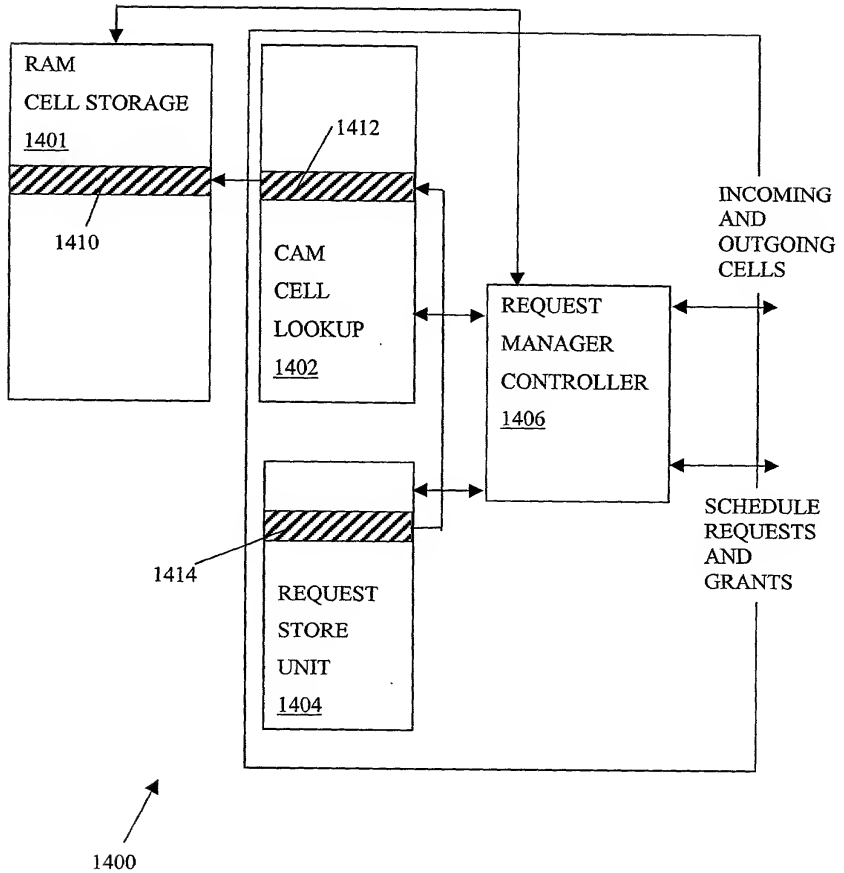


FIGURE 14

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Cell Lookup CAM entry Format

Bit[0]	Valid
Bits[10:1]	Pointer
Bits[13:11]	Priority
Bits [24:14]	Destination Address

FIGURE 15

2025 RELEASE UNDER E.O. 14176

Unicast Request Store Entry Format

Bit[0]	Valid
Bits[7:1]	Priority
Bits[19:8]	Destination Address0
Bits[23:20]	Request Count
Bits[33:24]	Head Pointer
Bits[43:34]	Tail Pointer
Bit[44]	Queue Over Limit
Bit[45]	Flow Control

FIGURE 16A

Bit[0]	Valid
Bits[3:1]	Priority
Bits[15:4]	Destination Address0
Bits[19:16]	Request Count
Bits[29:20]	Head Pointer
Bits[39:30]	Tail Pointer
Bit[40]	Flow Control

FIGURE 16B

Multicast Request Store Entry Format

Bit[0]	Valid
Bits[3:1]	Priority
Bits[7:4]	Cell Requested
Bits[11:8]	Cell Sent
Bits[15:12]	Flow Control
Bits[25:16]	Cell Address
Bits[37:26]	Destination Address0
Bit[49:38]	Destination Address1
Bit[61:50]	Destination Address2
Bit[73:62]	Destination Address3

FIGURE 17

2025 RELEASE UNDER E.O. 14176

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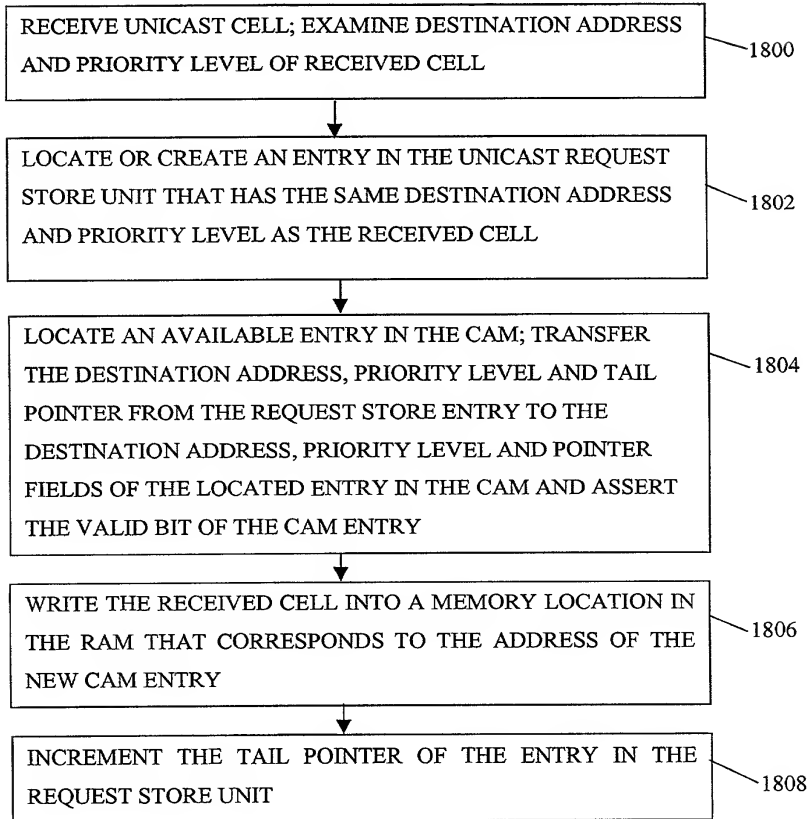


FIGURE 18

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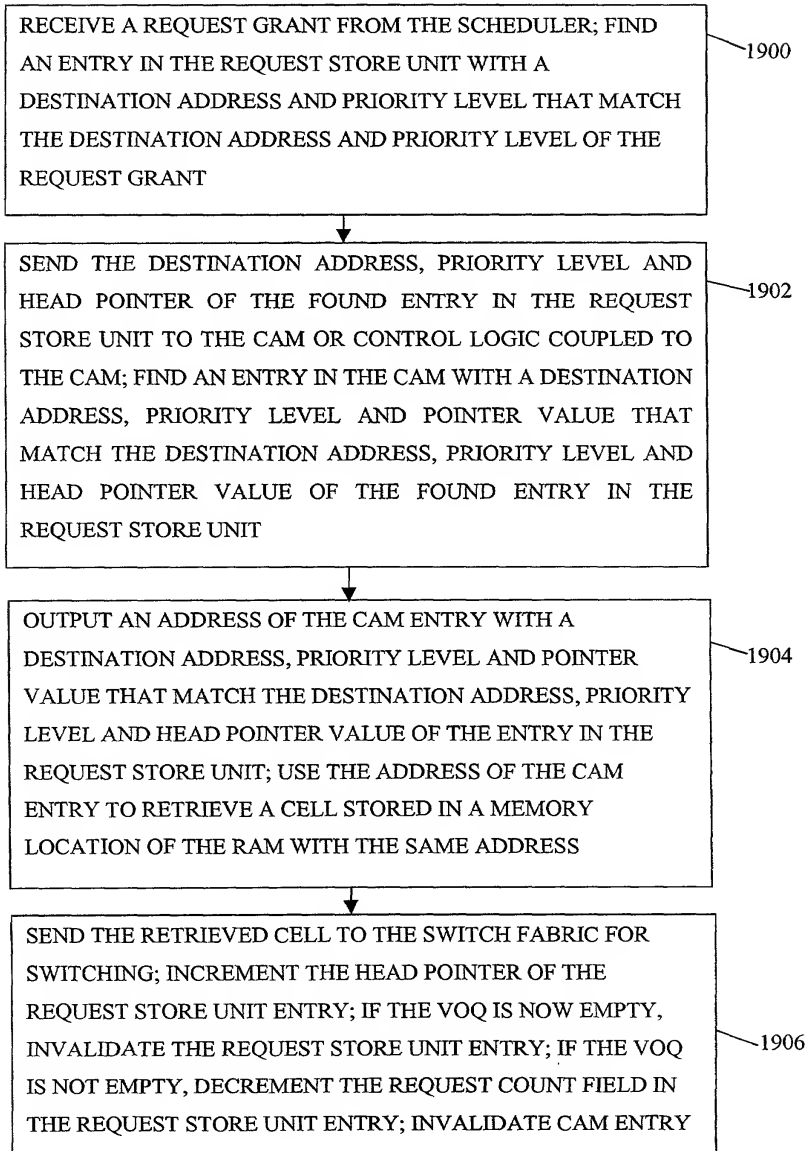


FIGURE 19